

V_{WM} = 5V, 0.8pF ESD Protection Array

FEATURES

Meet IEC61000-4-2(ESD) ±17kV(air) , ±12kV(contact)

Working Voltage: 5V

• Moisture sensitivity level: level 1, per J-STD-020

RoHS Compliant

• Halogen-free according to IEC 61249-2-21

APPLICATIONS

• USB 2.0 / 3.0 / 3.1

• High definition Multi-Media Interface(HDMI 1.3 /1.4 / 2.0)

MECHANICAL DATA

• Case: 2510P10

• Molding compound meets UL 94V-0 flammability rating

• Terminal: Matte tin plated leads, solderable per J-STD-002

• Meet JESD 201 class 1A whisker test

Polarity: As marked

Weight: 3.59mg (approximately)

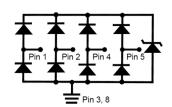
KEY PARAMETERS			
PARAMETER	VALUE	UNIT	
P _{PPSM}	95	W	
I _{PP}	5	Α	
V_{WM}	5	V	
$V_{(BR)}$ at $I_R = 1mA$	6	V	
V_C at $I_{PP} = 5A$	19	V	
Package	2510P10		
Configuration	Array		





2510P10

1. I/O 2. I/O 3. GND 4. I/O 5. I/O 6. NC 7. NC 8. GND 9. NC 10.NC



PARAMETER	SYMBOL	TESD5V0V4UA	UNIT	
Marking code on the device		24A		
Rated random recurring peak Impulse power dissipation (tp = 8/20µs waveform)	P _{PPSM}	95	W	
Peak impulse current (tp = 8/20µs waveform)	I _{PP}	5	Α	
ESD per IEC 61000-4-2 (Air)		±17		
ESD per IEC 61000-4-2 (Contact)	- V _{ESD}	±12	- kV	
Junction temperature range	TJ	-55 to +125	°C	
Storage temperature range	T _{STG}	-55 to +125	°C	



TESD5V0V4UA Taiwan Semiconductor

ELECTRICAL SPECIFICATIONS (T _A = 25°C unless otherwise noted)						
PARAMETER	CONDITIONS	SYMBOL	MIN	TYP	MAX	UNIT
Forward voltage per diode ⁽¹⁾	$I_R = 1mA$	$V_{(BR)}$	6	-	-	V
Rated working standoff voltage		V _{WM}	-	-	5	V
Reverse current ⁽¹⁾ $V_R = 5V$ (any I/O pin to Ground)		I _R	-	-	1	μΑ
Q1 (2)	$I_{PP} = 1 A$ (any I/O pin to Ground)	.,	-	-	15	V
Clamping voltage ⁽²⁾	I _{PP} = 5A (any I/O pin to Ground)	V _C	-	-	19	V
lunation and attack	1MHz, $V_R = 0V$ (any I/O pin to Ground)		-	-	0.8	pF
Junction capacitance	1MHz, $V_R = 0V$ (between I/O pins)	C	-	-	0.4	pF

Notes:

- 1. Pulse test with PW = 30ms
- 2. $tp = 8/20\mu s$ waveform

ORDERING INFORMATION			
ORDERING CODE	PACKAGE	PACKING	
TESD5V0V4UA RDG	2510P10	3K / 7" Reel	



CHARACTERISTICS CURVES

(T_A = 25°C unless otherwise noted)

Fig.1 8/20µs pulse waveform

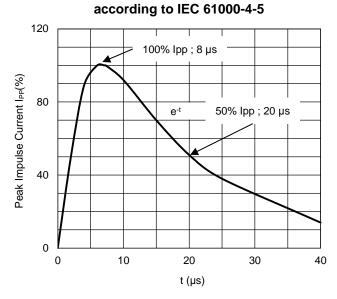


Fig.2 ESD pulse waveform according to IEC 6100-4-2

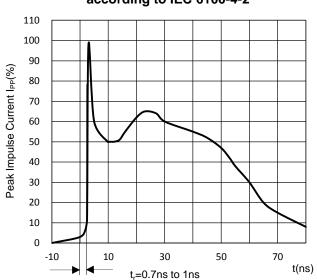


Fig.3 TLP I-V Curve

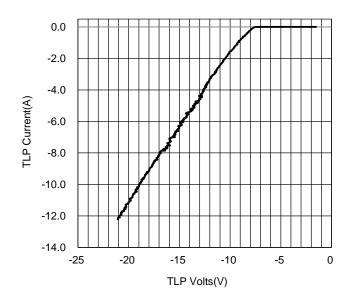
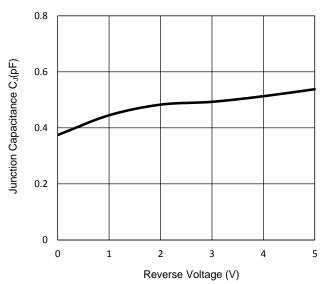


Fig.4 Typical Junction Capacitance (any I/O pin to Ground)

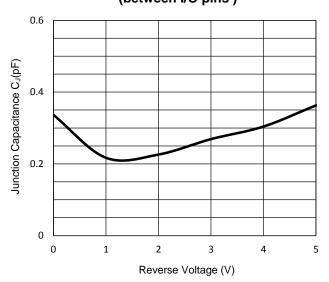




CHARACTERISTICS CURVES

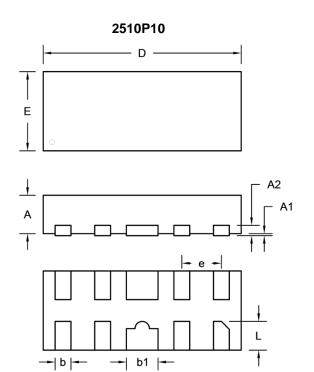
 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$

Fig.5 Typical Junction Capacitance (between I/O pins)



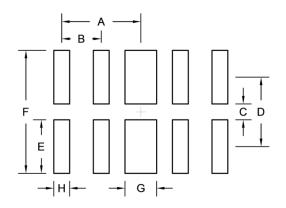


PACKAGE OUTLINE DIMENSIONS



DIM.	Unit (mm)		Unit ((inch)
Dilvi.	Min.	Max.	Min.	Max.
Α	0.46	0.51	0.018	0.020
A1	0.00	0.05	0.000	0.002
A2	0.13		0.005	
b	0.15	0.25	0.006	0.010
b1	0.35	0.45	0.014	0.018
D	2.40	2.60	0.094	0.102
E	0.90	1.10	0.035	0.043
е	0.50		0.	020
L	0.30	0.425	0.012	0.017

SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
Α	1.000	0.039
В	0.500	0.020
С	0.200	0.008
D	0.875	0.034
E	0.675	0.027
F	1.550	0.061
G	0.400	0.016
Н	0.200	0.008

MARKING DIAGRAM

. 24A

24A = Marking Code



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