

650 V, 6 A Silicon Carbide Schottky Diode

Features

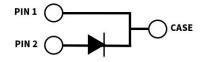
- 650-Volt Schottky rectifier
- Zero reverse recovery current
- Zero forward recovery voltage
- High-frequency operation
- Temperature-independent switching behavior
- Extremely fast switching
- Positive temperature coefficient on V_F







TO-252-2



Package Types: TO-252-2

PN: C3D06065

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Applications

- Switch mode power supplies (SMPS)
- Boost diodes in PFC or DC/DC stages
- Free wheeling diodes in inverter stages
- AC/DC converters

Benefits

- Replace bipolar with unipolar rectifiers
- Essentially no switching losses
- Higher efficiency
- Reduction of heat sink requirements
- Parallel devices without thermal runaway

Maximum Ratings (T_c = 25 °C Unless Otherwise Specified)

Parameter	Symbol	Value	Unit	Test Conditions	Note	
Repetitive Peak Reverse Voltage	V _{RRM}	650				
Surge Peak Reverse Voltage	V _{RSM}	650	V			
DC Blocking Voltage	V _{DC}	650				
	I _F	20	A	T _c =25 °C	Fig. 3	
Continuous Forward Current		9.5		T _C = 135 °C		
		6		T _c = 157 °C		
Repetitive Peak Forward Surge Current	I _{FRM}	28		T _C = 25 °C, t _P = 10 ms, Half Sine Wave		
		19		T _c = 110 °C, t _P = 10 ms, Half Sine Wave		
Non-Repetitive Peak Forward Surge Current	I _{FSM}	63		T _C = 25 °C, t _P = 10 ms, Half Sine Wave	T:~ 0	
		49		T _c =110 °C, t _P =10 ms, Half Sine Wave	Fig. 8	
Non-Repetitive Peak Forward Surge Current	I _{F, Max}	540		T_c = 25 °C, t_p = 10 μ s, Pulse	T:~ 0	
		460		T _c = 110 °C, t _p = 10 μs, Pulse	Fig. 8	
Power Dissipation	P _{tot}	100	W	T _c =25 °C	Fig. 4	
		43		T _c =110 °C		
Operating Junction and Storage Temperature	T _J , T _{stg}	-55 to +175	°C			

Electrical Characteristics

Parameter	Symbol	Тур.	Max.	Unit	Test Conditions	Note	
Forward Voltage	V _F	1.5	1.7	V	I _F = 6 A, T _J = 25 °C	Fig. 1	
		2.0	2.4		I _F = 6 A, T _J = 175 °C		
Deviage Current	8 40		V _R = 650 V, T _J = 25 °C	F: 2			
Reverse Current	I _R	15.5	160	μΑ	V _R = 650 V, T _J = 175 °C	Fig. 2	
Total Capacitive Charge	Q _c	15		nC	$V_R = 400 \text{ V, } I_F = 6 \text{ A}$ $di/dt = 500 \text{ A}/\mu\text{S}$ $T_J = 25 \text{ °C}$	Fig. 5	
Total Capacitance C 295 p 25.5 25.5 p		$V_R = 0 \text{ V}, T_J = 25 \text{ °C}, f = 1 \text{ MHz}$					
	С	28.5		pF	$V_R = 200 \text{ V}, T_J = 25 \text{ °C}, f = 1 \text{ MHz}$	Fig. 6	
		25.5			$V_R = 400 \text{ V}, T_J = 25 \text{ °C}, f = 1 \text{ MHz}$		
Capacitance Stored Energy	E _c	2.3		μJ	V _R = 400 V	Fig. 7	

Note: This is a majority carrier diode, so there is no reverse recovery charge.

Thermal Characteristics

Parameter	Symbol	Тур.	Unit	Note
Thermal Resistance from Junction to Case	$R_{\theta JC}$	1.5	°C/W	Fig. 9

Typical Performance

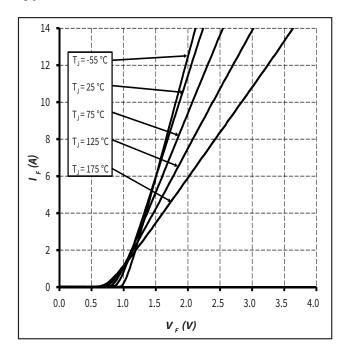
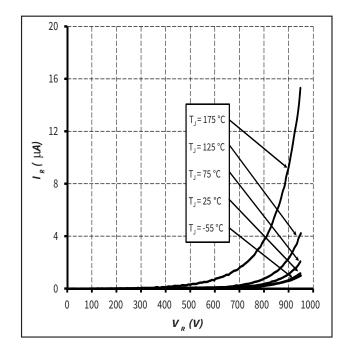


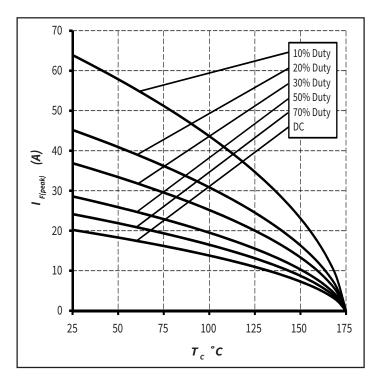
Figure 1. Forward Characteristics



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Figure 2. Reverse Characteristics

Typical Performance



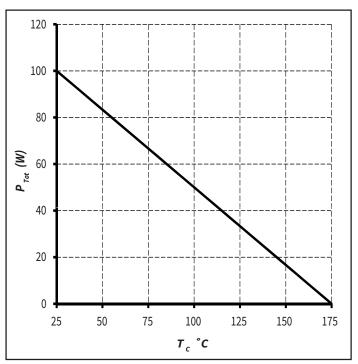
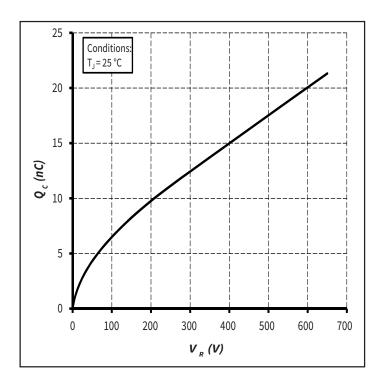


Figure 3. Current Derating

Figure 4. Power Derating





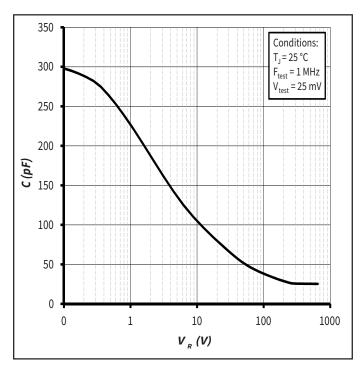
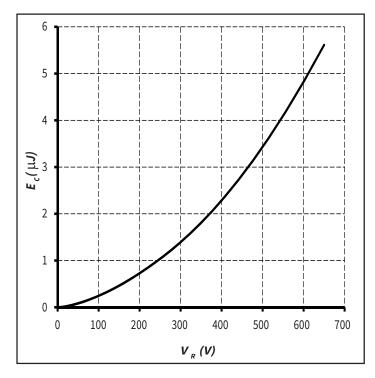


Figure 6. Capacitance vs. Reverse Voltage

Typical Performance



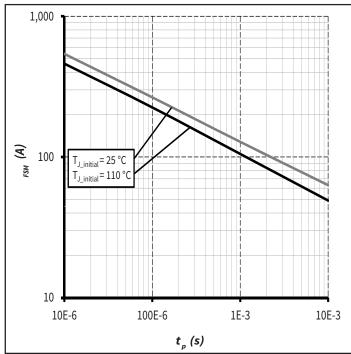


Figure 7. Capacitance Stored Energy

Figure 8. Non-Repetitive Peak Forward Surge Current Versus Pulse Duration (Sinusoidal Waveform)

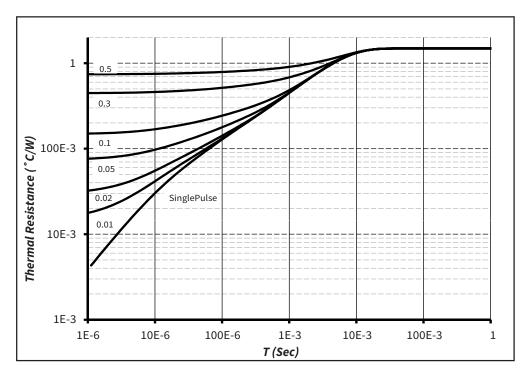
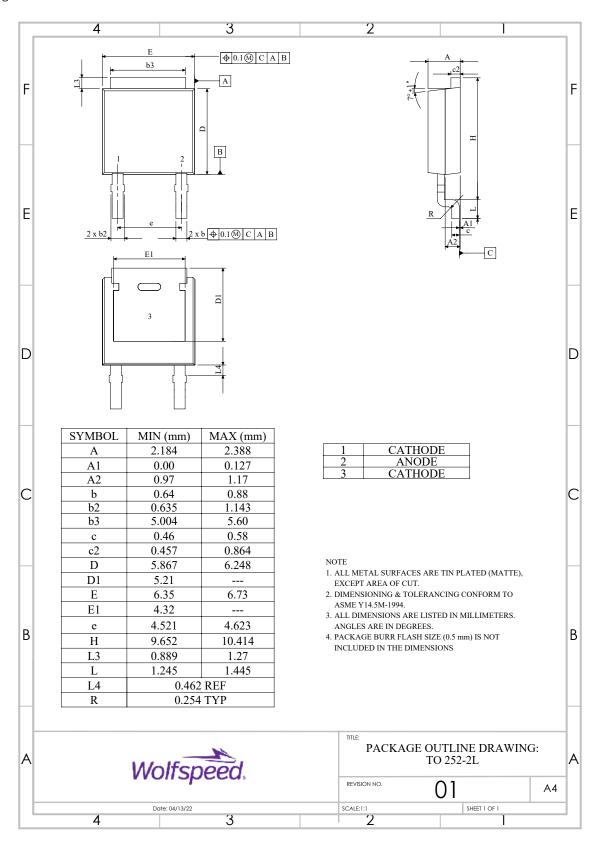


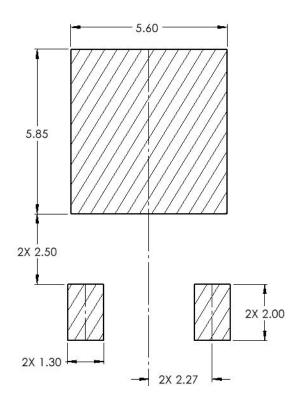
Figure 9. Transient Thermal Impedance

Package Dimensions

Package: TO-252-2



Recommended Solder Pad Layout



Part Number	Package	Marking
C3D06065E	TO-252-2	C3D06065

Diode Model

$$\begin{array}{c|c} & & & \\ \hline \\ & & \\ \hline \\ & & \\ \hline \\ & & \\ V_T & & \\ \hline \\ & & \\ \\ & & \\ \hline \\ & & \\ \\ & & \\ \hline \\ & & \\ \\ & & \\ \hline \\ & & \\ \\ & & \\ \hline \\ & & \\ \\ & & \\ \hline \\ & & \\ \\ & & \\ \hline \\ & & \\ \\ & & \\ \hline \\ & & \\ \\ & & \\ \hline \\ & & \\ \\ & & \\ \hline \\ & & \\ \\ & & \\ \hline \\ & & \\ \\ & & \\ \hline \\ & & \\ \\ \\ & & \\ \hline \\ \\ & & \\ \hline \\ & \\ \\ & & \\ \hline \\$$

$$Vf_T = V_T + If * R_T$$

$$V_T = 0.96 + (T_3 * -1.1*10^{-3})$$

 $R_T = 0.07 + (T_3 * 7.4*10^{-4})$

Note: T_j = Diode Junction Temperature In Degrees Celsius, valid from 25°C to 175°C

Revision History

Current Revision Date of Release		Description of Changes		
5	September-2023	Updated Wolfspeed branding, package drawing, and solder pad lay- out, Removed AEC-Q101 banner		
6	October-2023	Corrected solder pad layout and diode model		

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