



Product Change Notification: DSNO-03IXNL537

Date:

20-May-2025

Product Category:

Memory

Notification Subject:

CCB 7364.022 Initial Notice: Qualification of Microchip Technology Gresham – Fab 4 (GRTM) as a new fabrication location for the selected 24AA00xx, 24AA65, 24C00, 24C65, 24LC00, 24LC21xx, 24LC22A, 24LC65, 24LCS21A, 24LCS22A, 25AA040, 25AA080, 25AA160, 25C040, 25C080, 25C160, 25LC040, 25LC080, 25LC160, 93AA46, 93AA56, 93AA66, 93AA76, 93AA86, 93C46B, 93C56, 93C66xx, 93C76, 93C86, 93LC46xx, 93LC56, 93LC66xx, 93LC76 and 93LC86 device families of 77k technology available in various packages.

Affected CPNs:

[DSNO-03IXNL537_Affected_CPN_05202025.pdf](#)

[DSNO-03IXNL537_Affected_CPN_05202025.csv](#)

PCN Status: Initial Notification

PCN Type: Manufacturing Change

Microchip Parts Affected: Please open one of the files found in the Affected CPNs section.

Note: For your convenience Microchip includes identical files in two formats (.pdf and .xls)

Description of Change: Qualification of Microchip Technology Gresham – Fab 4 (GRTM) as a new fabrication location for the selected 24AA00xx, 24AA65, 24C00, 24C65, 24LC00, 24LC21xx, 24LC22A, 24LC65, 24LCS21A, 24LCS22A, 25AA040, 25AA080, 25AA160, 25C040, 25C080, 25C160, 25LC040, 25LC080, 25LC160, 93AA46, 93AA56, 93AA66, 93AA76, 93AA86, 93C46B, 93C56, 93C66xx, 93C76, 93C86, 93LC46xx, 93LC56, 93LC66xx, 93LC76 and 93LC86 device families of 77k technology available in various packages.

Pre and Post Summary Changes:

	Pre Change	Post Change
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Fabrication Site	Microchip Technology Tempe – Fab 2 (TMGR)	Microchip Technology Gresham – Fab 4 (GRTM)
Wafer Size	8"	8"

Impacts to Datasheet: None

Change Impact: None

Reason for Change: To improve manufacturability and on time delivery performance by qualifying a new fabrication location (GRTM - FAB 4), which is a Microchip-owned facility that offers significant expansion potential to better meet future client demand. *Note: The attached file called Tempe_Fab2_IATF_Decertification is for the manufacturing site deactivation of Microchip Technology Tempe – Fab 2 (TMGR), contact your local Microchip sales office for inquiries.

Change Implementation Status: In Progress

Estimated Qualification Completion Date: November 2025

Note: Please be advised the qualification completion times may be extended because of unforeseen business conditions however implementation will not occur until after qualification has completed and a final PCN has been issued. The final PCN will include the qualification report and estimated first ship date. Also note that after the estimated first ship date guided in the final PCN customers may receive pre and post change parts.

Timetable Summary:

	May 2025					>	November 2025				
Work Week	18	19	20	21	22		44	45	46	47	48
Initial PCN Issue Date				X							
Qual Report Availability								X			
Final PCN Issue Date								X			

Method to Identify Change: Traceability Code

Qualification Plan: Please open the attachments included with this PCN labeled as PCN_#_Qual_Plan.

Revision History: May 20, 2025: Issued initial notification

Note: The change described in this PCN does not alter Microchip's current regulatory compliance regarding the material content of the applicable product.

Attachments:

[Tempe_Fab2_IATF_Decertification.pdf](#)
[PCN_DSNO-03IXNL537_Qualification_Plan_RevA.pdf](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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