



12500 TI Boulevard, MS 8640, Dallas, Texas 75243

PCN# 20240502009.1
Datasheet for INA117
Change Notification

Date: May 06, 2024
To:

Dear Customer:

This is a notice of change to a product data sheet for a device that is currently offered by Texas Instruments. The details of this change are on the following pages.

We request you acknowledge receipt of this notification within **30** days of the date of this notice. Lack of acknowledgement of this notice within 30 days constitutes acceptance and approval of this change. If samples or additional data are required, requests must be received within **30 days** of this notification.

The proposed first ship date is indicated on page 3 of this notification, unless customer agreement has been reached on an earlier implementation of the change.

This notice does not change the end-of-life status of any product. Should product affected be on a previously issued product withdrawal/discontinuance notice, this notification does not extend the life of that product or change the life time buy offering/discontinuance plan.

For questions regarding this notice, contact your local Field Sales Representative or the Change Management team.

Sincerely,

Change Management Team
SC Business Services

20240502009.1
Data Sheet Change Notification
Attachments

Products Affected:

The devices listed on this page are a subset of the complete list of affected devices. According to our records, you have recently purchased these devices. The corresponding customer part number is also listed, if available.

DEVICE	CUSTOMER PART NUMBER
INA117KU	NULL
INA117KU/2K5	NULL
INA117P	NULL

Technical details of this Product Change follow on the next page(s).

PCN Number:		20240502009.1				PCN Date:		May 06, 2024		
Title:		Datasheet for INA117								
Customer Contact:		Change Management team			Dept:		Quality Services			
Proposed 1 st Ship Date:		August 04, 2024								
Change Type:										
☐	Assembly Site			☐	Design			☐	Wafer Bump Material	
☐	Assembly Process			☒	Data Sheet			☐	Wafer Bump Process	
☐	Assembly Materials			☐	Part number change			☐	Wafer Fab Site	
☐	Mechanical Specification			☐	Test Site			☐	Wafer Fab Materials	
☐	Packing/Shipping/Labeling			☐	Test Process			☐	Wafer Fab Process	

PCN Details

Description of Change:

The product datasheet(s) is being updated as summarized below.
The following change history provides further details.



INA117
SBOS154B – SEPTEMBER 2000 – REVISED APRIL 2024

Changes from Revision A (November 2000) to Revision B (April 2024)

Page

• Updated the formatting for tables, figures, and cross-references throughout the document.....	1
• Deleted information about the INA117AM and INA117SM variants throughout this document.....	1
• Changed pin 8 from "Comp" to "NC" in the <i>Description</i> and <i>Pin Configuration and Functions</i> sections.....	1
• Added <i>Package Information</i> table to the <i>Description</i> section.....	1
• Added <i>Pin Functions</i> table.....	2
• Added ESD Ratings table.....	3
• Added single supply specification to <i>Recommended Operating Conditions</i>	3
• Added specified temperature range to <i>Recommended Operating Conditions</i>	3
• Added VREF = 0V, VCM = VS/2, and G = 1 to "unless otherwise noted" conditions in <i>Electrical Characteristics</i> and <i>Typical Characteristics</i> for clarity.....	4
• Changed parameter from "Offset voltage vs Temperature" to "Offset voltage drift" in <i>Electrical Characteristics</i>	4
• Added test condition of "TA = –40°C to +85°C" for "Offset voltage drift" in <i>Electrical Characteristics</i>	4
• Changed parameter from "Offset Voltage vs Power Supply" to "Power-supply rejection ratio" in <i>Electrical Characteristics</i>	4
• Added test condition of "TA = –40°C to +85°C" for "CMRR" in <i>Electrical Characteristics</i>	4
• Changed "Common-mode input impedance" typical value from 400kΩ to 200kΩ in <i>Electrical Characteristics</i>	4
• Added test condition "TA = –40°C to +85°C" for "Gain error vs temperature" in <i>Electrical Characteristics</i> and renamed to "Gain error drift" for clarity.....	4
• Changed "Gain nonlinearity" typical value from 0.0002% to 0.0005% in <i>Electrical Characteristics</i>	4
• Added test condition "Continuous to VS/2" to Short-circuit current specification in <i>Electrical Characteristics</i> for clarity.....	4
• Change minimum Slew rate from 2V/μs to 1.7V/μs in <i>Electrical Characteristics</i>	4
• Deleted redundant voltage range, operating temperature range, and specification temperature range specifications from <i>Electrical Characteristics</i>	4
• Deleted <i>Reducing Differential Gain</i> application circuit figure	11
• Added <i>Documentation Support</i> and <i>Related Documentation</i> sections.....	17

The datasheet number will be changing.

Device Family	Change From:	Change To:
INA117	SBOS154A	SBOS154B

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/INA117>

Reason for Change:			
To accurately reflect device characteristics.			
Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):			
Electrical specification performance changes as indicated above.			
Changes to product identification resulting from this PCN:			
None.			
Product Affected:			
INA117KU	INA117KU/2K5	INA117KU/2K5G4	INA117P

For questions regarding this notice, e-mails can be sent to the Change Management team or your local Field Sales Representative.

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